

X-1478 US
10/787,326

PATENT
Conf. No.: 3972

IN THE UNITED STATES PATENT OFFICE

Applicants: Guenter Stenz et al.
Assignee: Xilinx, Inc.
Title: Method System and Apparatus for Floorplanning Programmable Logic Designs
Serial No.: 10/787,326 File Date: 02-26-04
Examiner: Vuthe Siek Art Unit: 2825
Docket No.: X-1478 US Conf. No.: 3972

Mail Stop AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the Patent Office on June 5, 2006, please replace/substitute the following paragraph(s)/section(s)/claim(s) as indicated.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.